

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Kevin D. Safford et al.

Confirmation No.:

Application No.:

Examiner:

Filing Date: Nov. 14, 2003

Group Art Unit:

Title: ERROR DETECTION METHOD AND SYSTEM FOR PROCESSORS THAT EMPLOY
LOCKSTEPPED CONCURRENT THREADS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. EV336575092US

Date of Deposit Nov. 14, 2003

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: Eric Ho

Respectfully submitted,

Kevin D. Safford et al.

By

Eric Ho

Attorney/Agent for Applicant(s)
Reg. No. 39,711

Date: Nov. 14, 2003

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200206543-1		
	APPLICANT		
	K vin D. Safford et al.		
	FILING DATE	GROUP	
	Nov. 14, 2003		

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	2001/0037447	Nov. 1, 2001	Mukherjee et al.	
	1B	2001/0034827	Oct. 25, 2001	Mukherjee et al.	
	1C	2001/0034824	Oct. 25, 2001	Mukherjee et al.	
	1D	2001/0034854	Oct. 25, 2001	Mukherjee	
	1E	2001/0037445	Nov. 1, 2001	Mukherjee	
	1F	2002/0023202	Feb. 21, 2002	Mukherjee	
	1G	2001/0037448	Nov. 1, 2001	Mukherjee et al.	
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	T. N. Vijaykumar, Irith Pomeranz, and Karl Cheng, "Transient-Fault Recovery Using Simultaneous Multithreading," School of Electrical and Computer Engineering, Purdue University, 29th Annual International Symposium on Computer Architecture (ISCA), (12 pages), 2002.
	1R	Shubhendu S. Mukherjee, Michael Kontz, and Steven K. Reinhardt, "Detailed Design and Evaluation of Redundant Multithreading Alternatives," 29th Annual International Symposium on Computer Architecture (ISCA), pages 1-12, 2002.
	1S	

EXAMINER

DATE CONSIDERED